

Protective Relaying for DER Integration

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2019 ESIG Spring Technical Conference DER WG Meeting Albuquerque, New Mexico, March 19



Protective Relaying for DER Integration

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- Operating earnings and EPS, which is earnings from continuing operations excluding non-service-related pension costs of our principal pension plans.
- GE Industrial operating & Verticals earnings and EPS, which is operating earnings of our industrial businesses and the GE Capital businesses that we expect to retain.
- GE Industrial & Verticals revenues, which is revenue of our industrial businesses and the GE Capital businesses that we expect to retain.
- Industrial segment organic revenue, which is the sum of revenue from all of our industrial segments less the effects of acquisitions/dispositions and currency exchange.
- Industrial segment organic operating profit, which is the sum of segment profit from all of our industrial segments less the effects of acquisitions/dispositions and currency exchange.
- Industrial cash flows from operating activities (Industrial CFOA), which is GE's cash flow from operating activities excluding dividends received from GE Capital.
- Capital ending net investment (ENI), excluding liquidity, which is a measure we use to measure the size of our Capital segment.
- GE Capital Tier 1 Common ratio estimate is a ratio of equity

Outline

- Why we need protection
- How we provide equipment protection
 Models and Examples
- How we provide System Protection
 Example



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Why We Need Protection?

- Prevent damages of power system components (Equipment Protection)
 - -From excessive currents over designed limits
 - -From excessive voltages over designed limits
- Prevent partial collapse or blackout of power system

(System Protection)

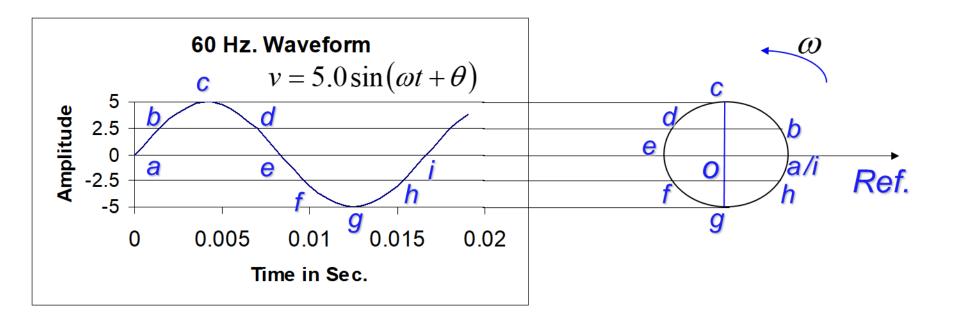


Protection Challenges with DER Integration

- Ensuring fault is isolated from all sources
- Maintaining security for varying fault current levels
- Avoiding desensitization of protective relaying
 Avoiding overvoltage
- Avoiding unnecessary DER unavailability due to
 - -transient faults
 - -permanent faults



Phasor

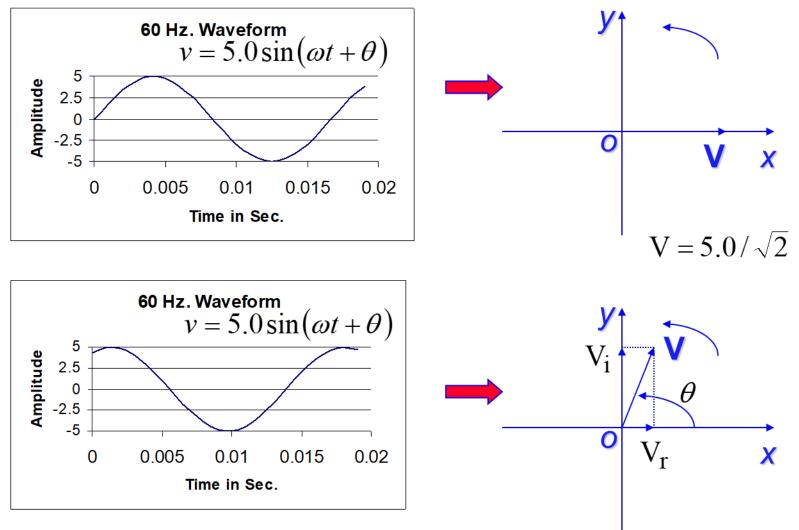


Time domain 🛛 📥 Frequency domain



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Phasor

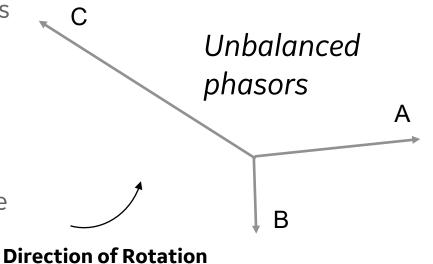


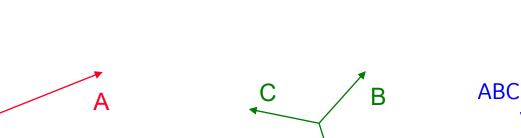


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Symmetrical Components

Symmetrical component theory as applied to three phase power systems recognizes that any unbalanced three phase system of phasors may be resolved into three symmetrical systems of phasors, namely positive, negative and zero sequence components.





Negative

Sequence



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B

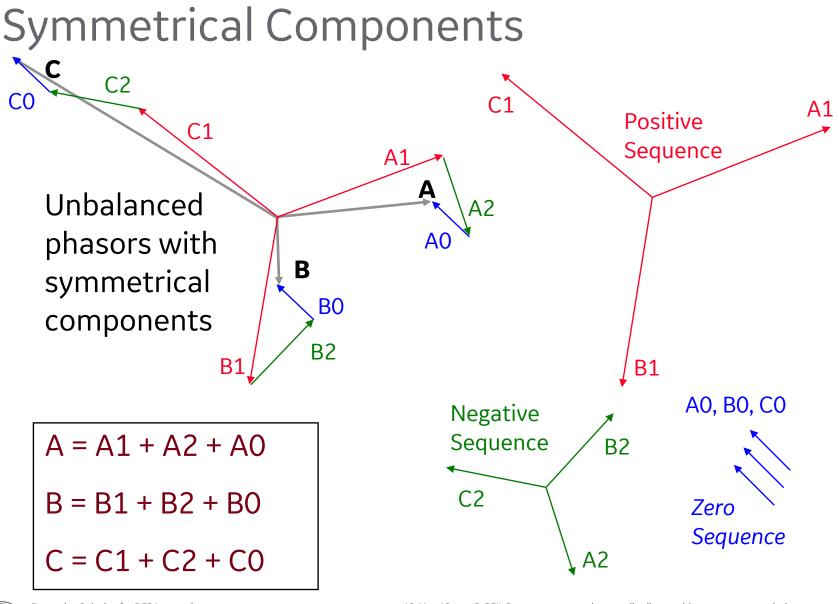
Positive

Sequence

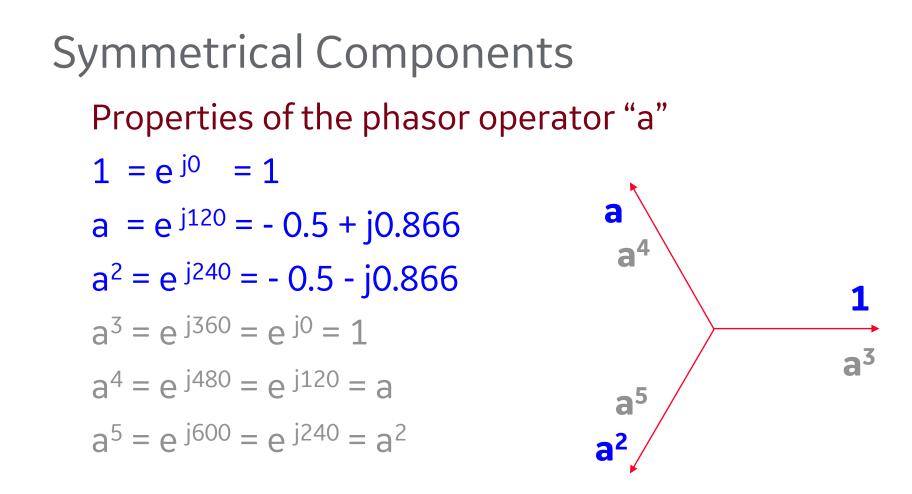
Α

7ero

Sequence



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- Operator "a" rotates a phasor 120° in the counter-clockwise direction
- Operator "-a" rotates a phasor 120⁰ in the clockwise direction



Symmetrical Components Analysis

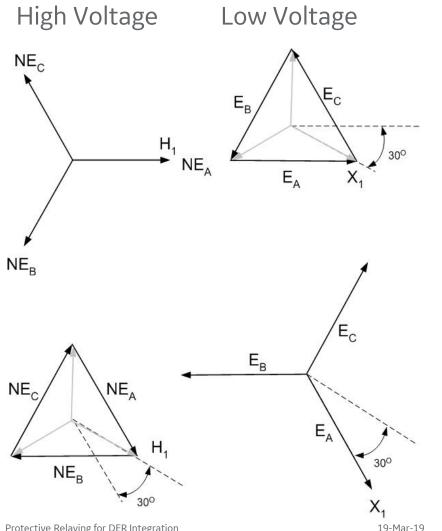
$$V_{A0} = 1/3 (V_A + V_B + V_C)$$
$$V_{A1} = 1/3 (V_A + aV_B + a^2V_C)$$
$$V_{A2} = 1/3 (V_A + a^2V_B + aV_C)$$

Synthesis

$$V_{A} = V_{A1} + V_{A2} + V_{A0}$$
$$V_{B} = V_{B1} + V_{B2} + V_{B0} = a^{2}V_{A1} + aV_{A2} + V_{A0}$$
$$V_{C} = V_{C1} + V_{C2} + V_{C0} = aV_{A1} + a^{2}V_{A2} + V_{A0}$$



ANSI Standard Connections for Transformers

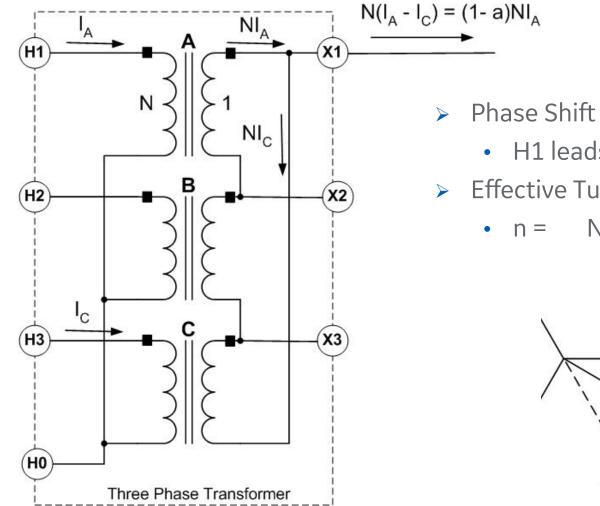


High voltage reference phase voltage leads the low voltage reference phase voltage by 30^o

- > Wye-delta
- Delta-wye

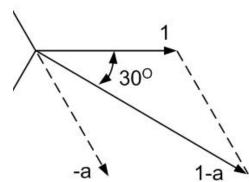


Wye-delta Connected Transformer



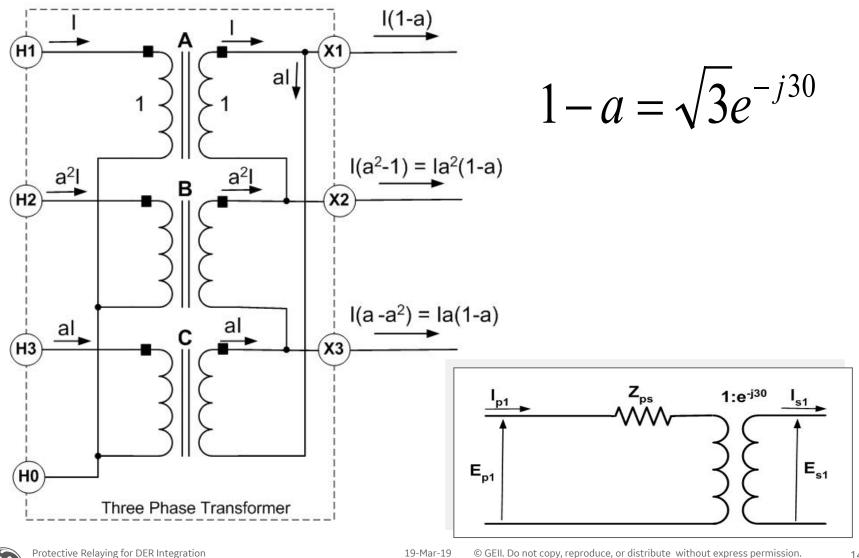
- H1 leads X1 by 30^o
- Effective Turns Ratio

• n = N
$$\sqrt{3}$$



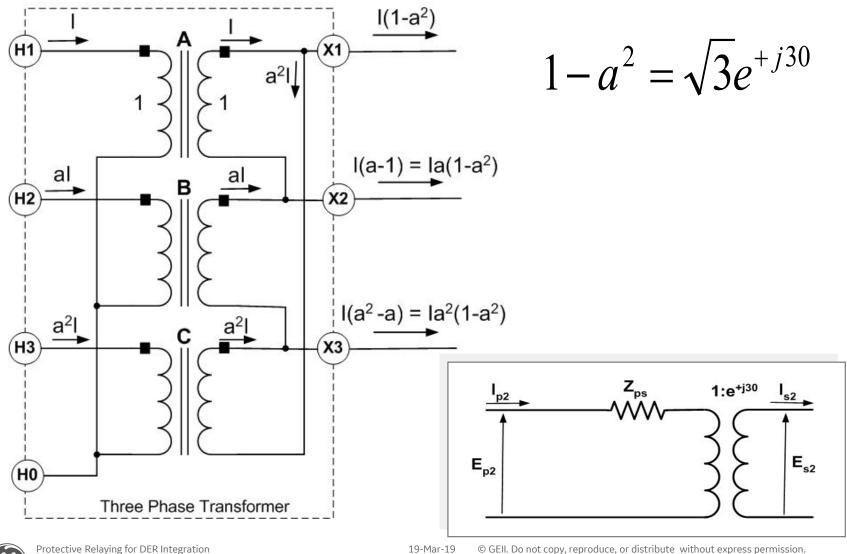


Positive Sequence Circuit



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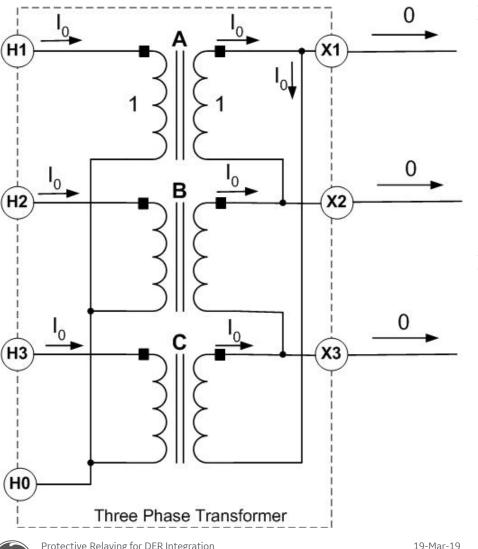
Negative Sequence Circuit



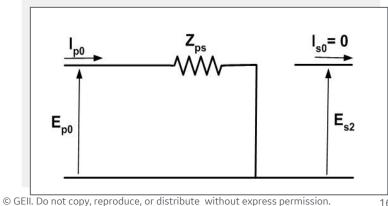
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Zero Sequence Circuit



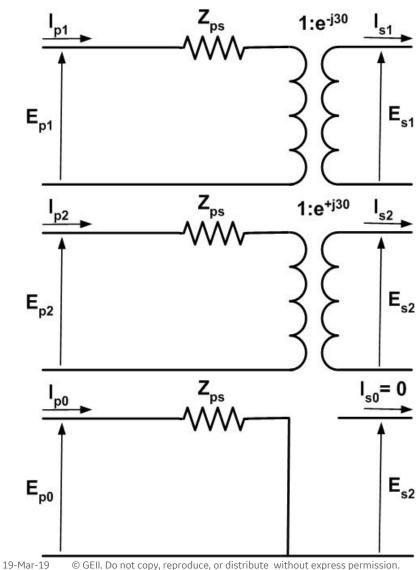
- Zero sequence current \geq
 - flows in the high voltage system side of H1
 - flows in the high voltage wye connected windings
 - circulates in the delta connected winding
- Zero sequence current does not flows in the low voltage system side of X1 (the delta side)



Sequence Circuits for Wye-ground-delta Transformers

Positive Sequence Network

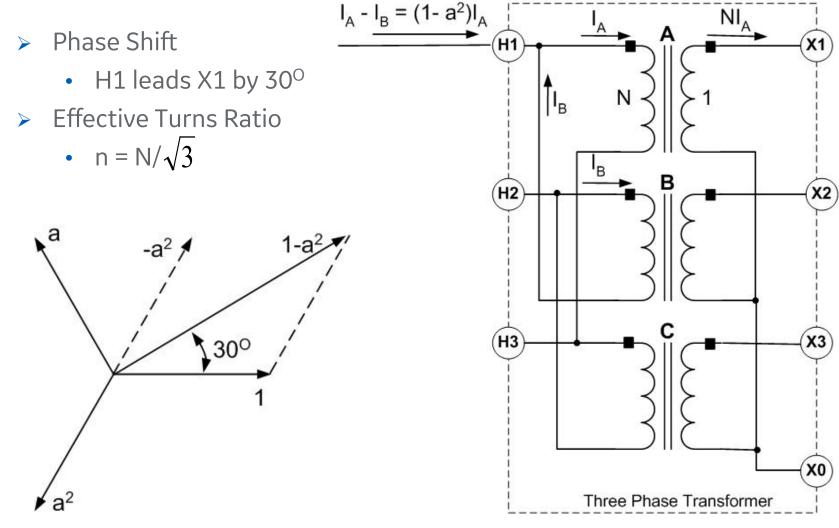
Negative Sequence Network



Zero Sequence Network



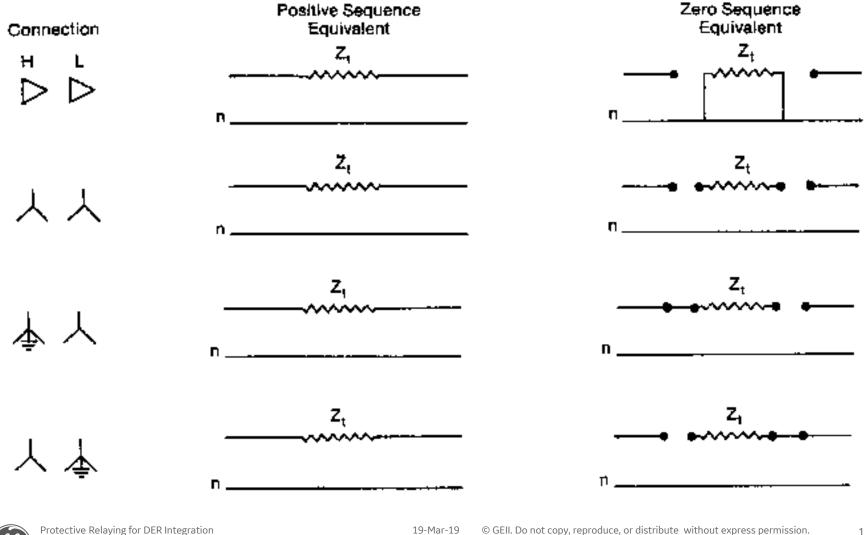
Delta-wye Connected Transformer



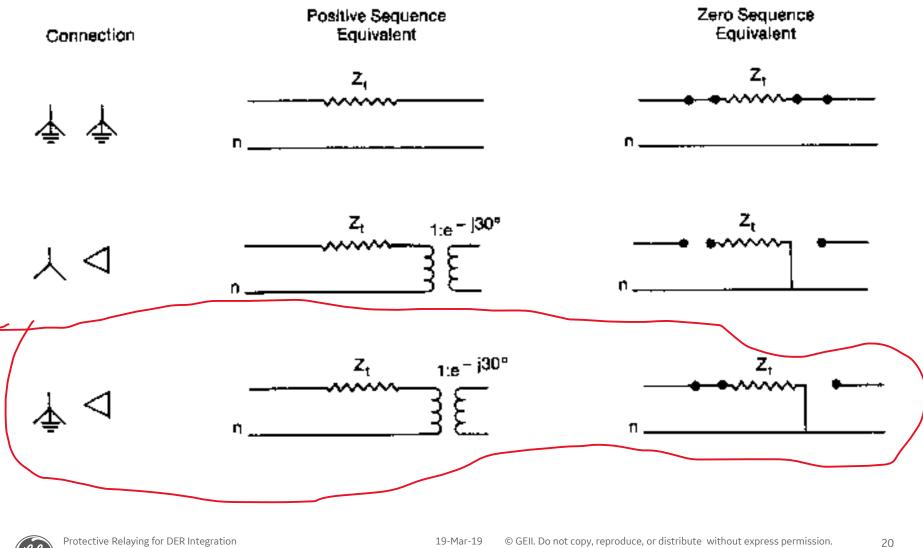


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Equivalent Circuits - Two Winding

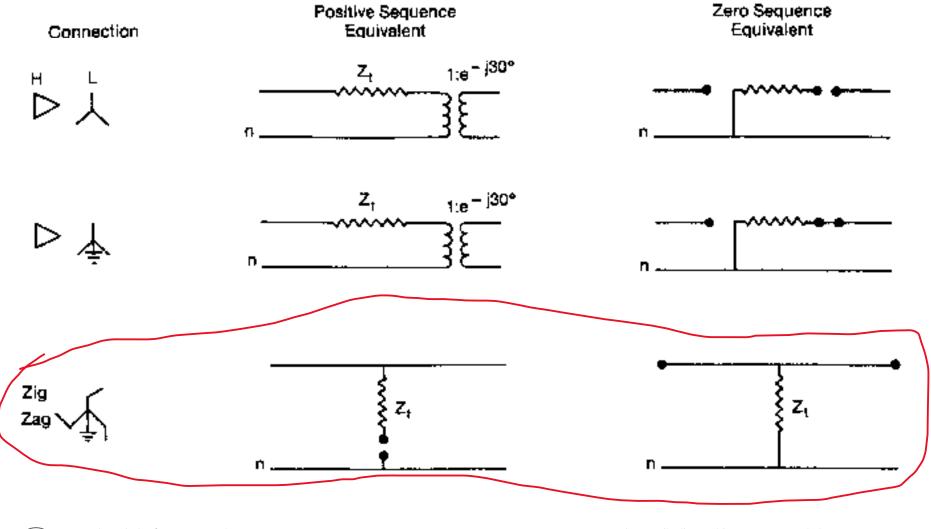


Equivalent Circuits - Two Winding



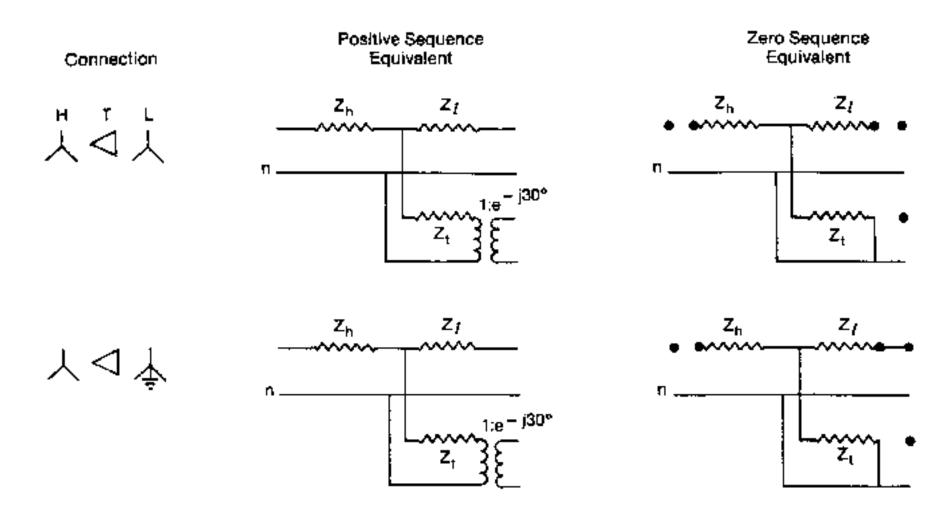
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Equivalent Circuits - Two Winding



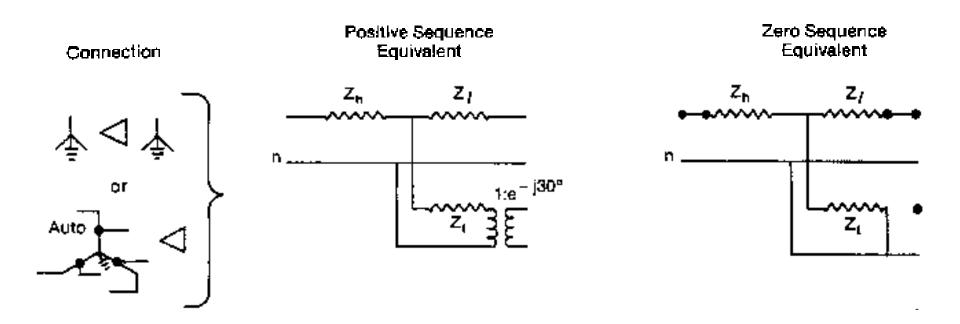
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Equivalent Circuits - Three Winding





Equivalent Circuits - Three Winding

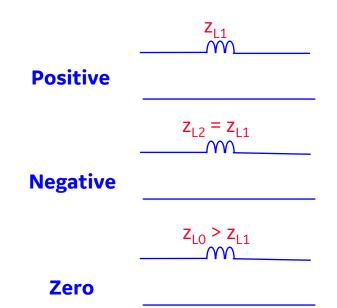




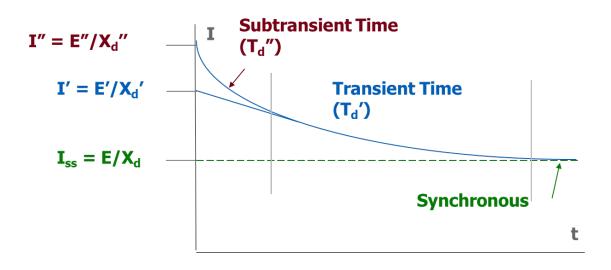
Symmetrical Components – Line Model

Z2 = Z1 for transposed lines. For non-transposed line, there may be some variance.

Z0 > Z1 due to mutual coupling between phases. Mutual coupling to adjacent circuits must be considered.



Symmetrical Components – Generator Short Circuit



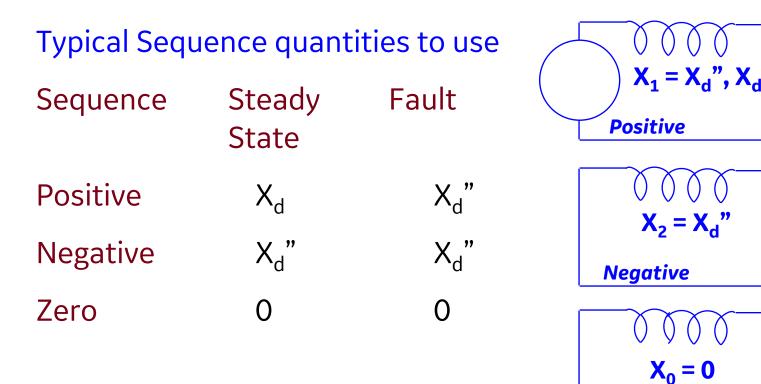
$$I_{ss} = (E''/X_d'' - E'/X_d') e^{-t/Td''} + (E'/X_d' - E/X_d) e^{-t/Td'} + E/X_d$$

- Modeling is very well understood and repeatable
 - Manufacturers provide test data for the required parameters
- Existing fault simulation software provides good modeling which has been validated with actual fault data
- Traditional protection relays are designed around these characteristics

- X'd Transient: Lasts**10-12** cycles
- Xs Synchronous:
 Fault current
 magnitude can range
 from 1.0 1.2 pu.
- Produces I2 and I0 current.
- Voltage source model



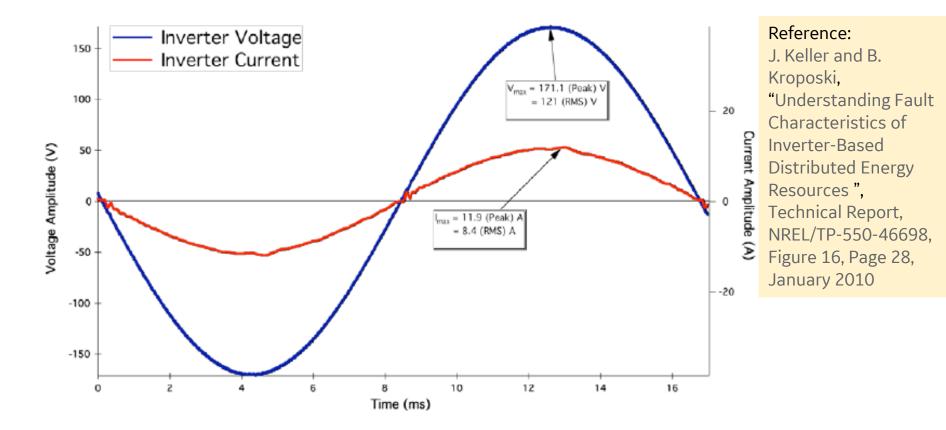
Symmetrical Components – **Generator Model**



26

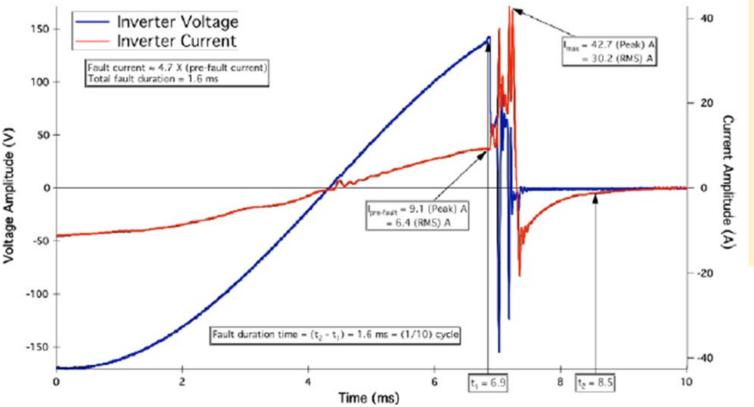
Zero

Symmetrical Components – Type 4, 1-Phase 1 kW Inverter Prefault Voltage & Current





Symmetrical Components – Type 4, 1-Phase 1 kW Inverter Fault Voltage & Current



Reference:

J. Keller and B. Kroposki, "Understanding Fault Characteristics of Inverter-Based Distributed Energy Resources ", Technical Report, NREL/TP-550-46698, Figure 17, Page 28, January 2010



Symmetrical Components – Type 4, 3-phase 500 kVA Inverter Fault Voltage & Current



Reference: J. Keller and B. Kroposki, "Understanding Fault Characteristics of Inverter-Based Distributed Energy Resources ", Technical Report, NREL/TP-550-46698, Figure 17, Page 28, January 2010

Fault current is approximately 2 to 3 times the rated peak output current with a duration time of approximately 1.1 to 4.25 ms for different fault types.



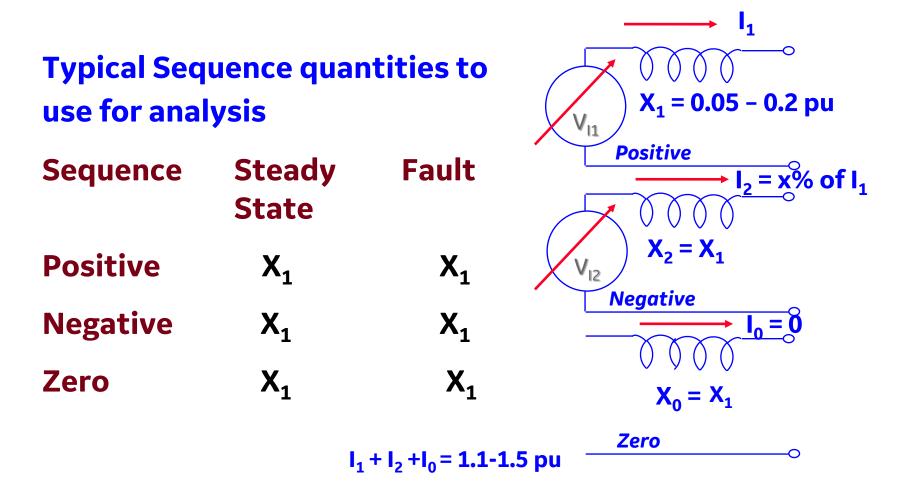
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Symmetrical Components – Inverter Model

- Do not have the rotational inertia of the rotor and excitation of the field
- Generally a current source with variable voltage support
- Traditional protection methods will not work
- Produce low level fault current typically 1.1-1.5 pu except for very short duration high transient current
 - Many inverter models do not produce I_2 or I_0
 - Fault characteristic depends on the inverter control
 - varies among manufacturers
 - Traditional fault software not configured to accurately represent inverters
 - Most simulation software presently use synchronous generator model
 - Modified to approximate the characteristics of an inverter
 - Still a voltage source model that does not adequately represent the actual fault current characteristics

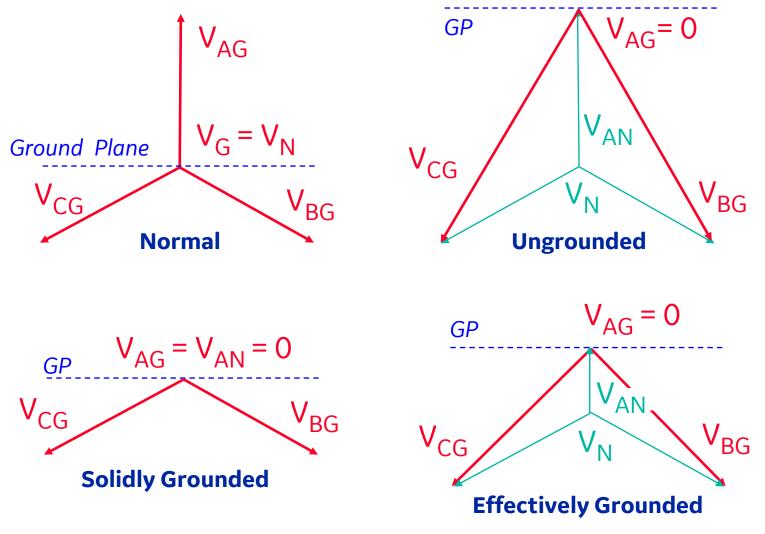


Symmetrical Components – Type 4 Inverter Model: *Current Source with Variable Voltage*





System Shunt Faults – Voltages



19-Mar-19

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Protection Challenges with DER Integration

- Ensuring fault is isolated from all sources
- Maintaining security for varying fault current levels
- Avoiding desensitization of protective relaying
 - -Avoiding overvoltage
- Avoiding unnecessary DER unavailability due to
 - -transient faults

– permanent faults



Protection Challenges with DER Integration

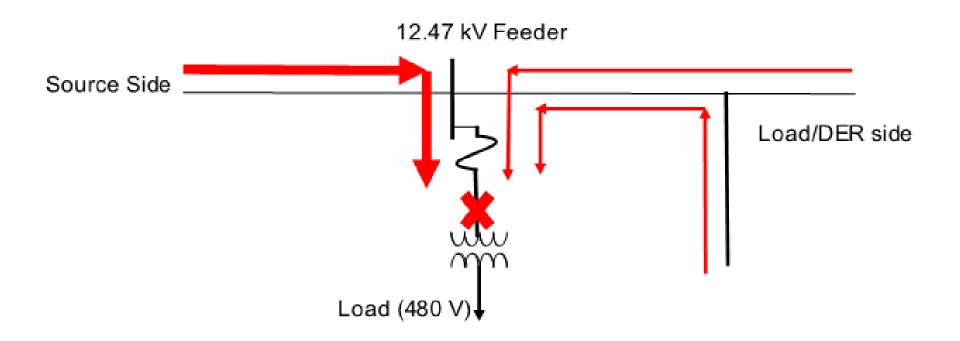
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Protection Issue: Case Study 1

Dependability for grid-connected and islanded mode



IEEE Power System Relaying and Control (PSRC) Committee WG C30 - working document. Work in progress.



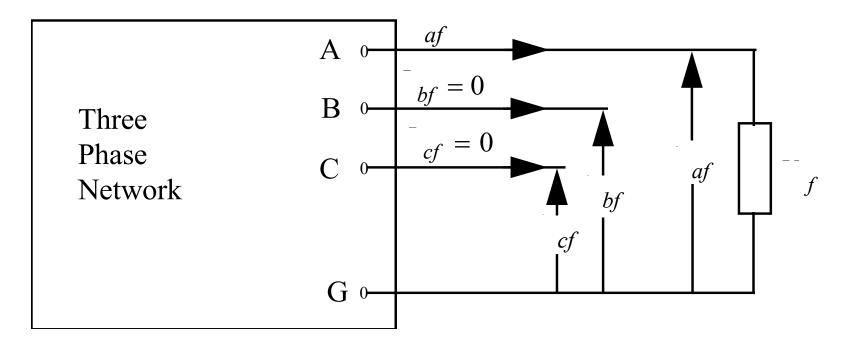
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Types of System Shunt Faults

Туре	Phase Combinations
Three-phase Three-phase-to-ground	ABC ABCG
Phase-to-ground	AG, BG, CG
Phase-to-phase	AB, BC, CA
Two-phase-to-ground	ABG, BCG, CAG



System Faults: Single-Phase-to-Ground



At fault point,

$$I_{bf} = I_{cf} = 0$$

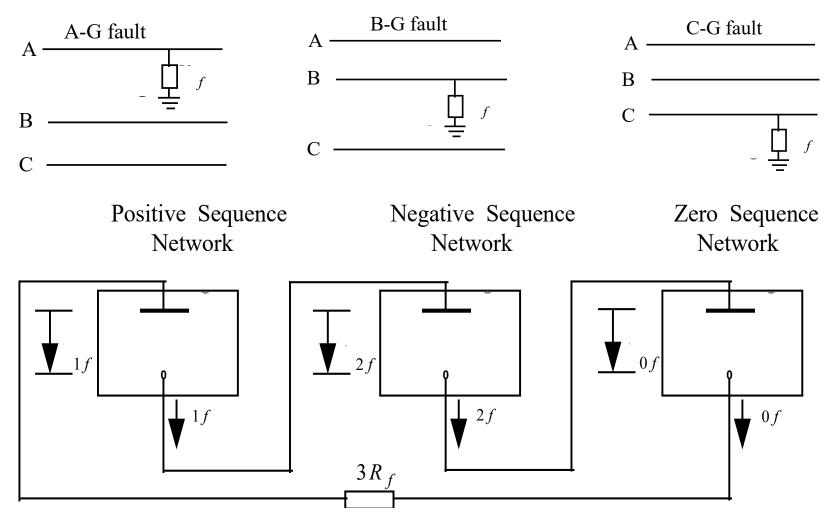
 $V_{af} = I_{af} R_f$

$$I_{0f} = I_{1f} = I_{2f} = \frac{1}{3} (I_{af})$$
$$V_{0f} + V_{1f} + V_{2f} = I_{0f} (3R_f)$$



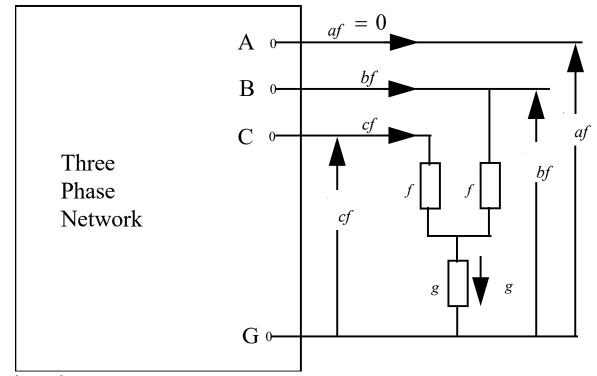
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System Faults: Single-Phase-to-Ground





System Faults: Two-Phase-to-Ground



At fault point,

$$I_{af} = 0 \quad \text{and} \quad I_{bf} + I_{cf} = I_g$$

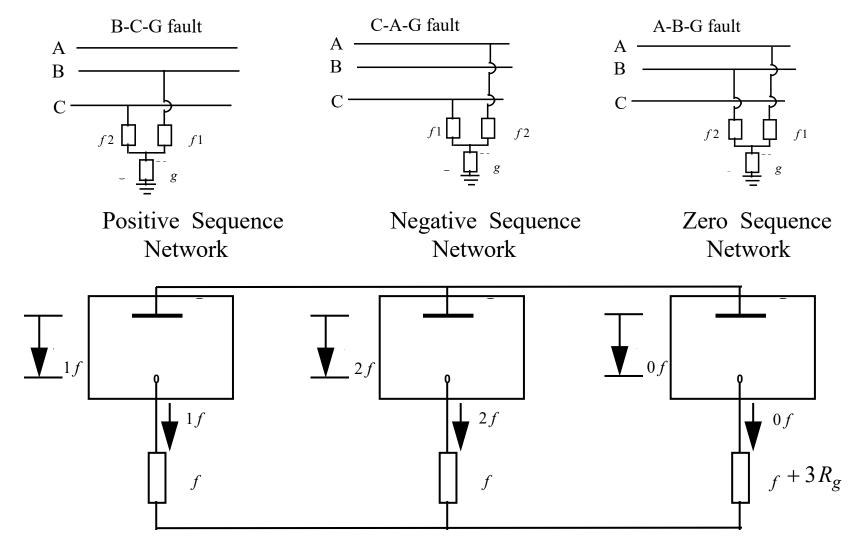
$$V_{bf} = I_{bf}R_f + I_gR_g \quad \longrightarrow (V_{1f} - I_{1f}R_f) = (V_{2f} - I_{2f}R_f) = [V_{0f} - I_{0f}(R_f + 3R_g)]$$

$$V_{cf} = I_{cf}R_f + I_gR_g$$



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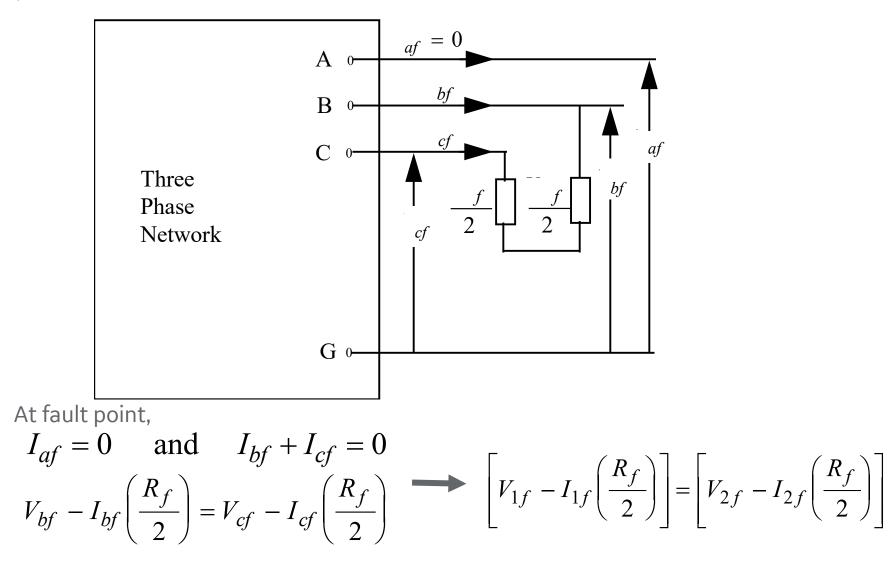
System Faults – Two-Phase-to-Ground





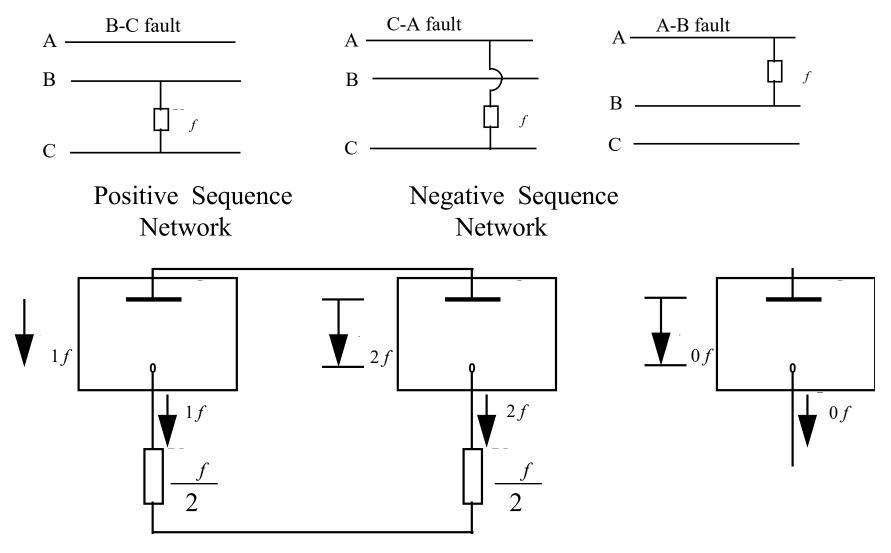
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System Faults: Phase-to-Phase





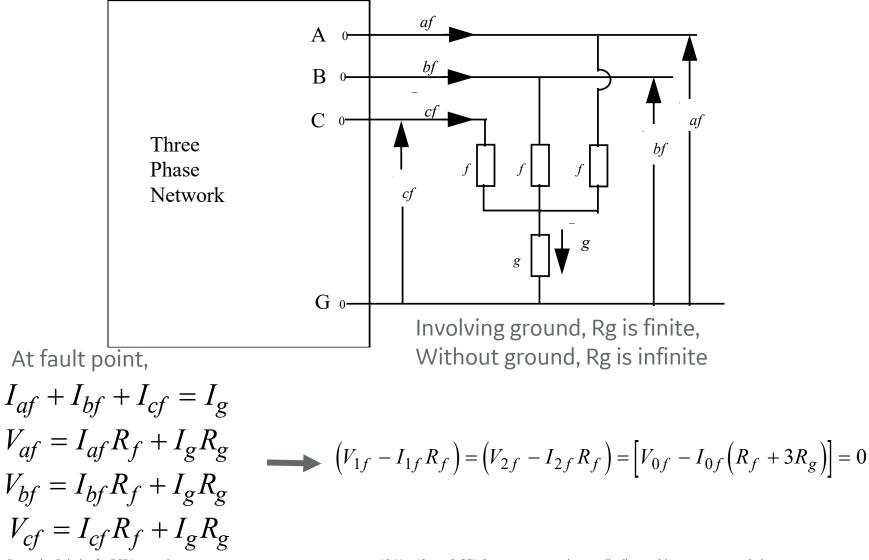
System Faults: Phase-to-Phase





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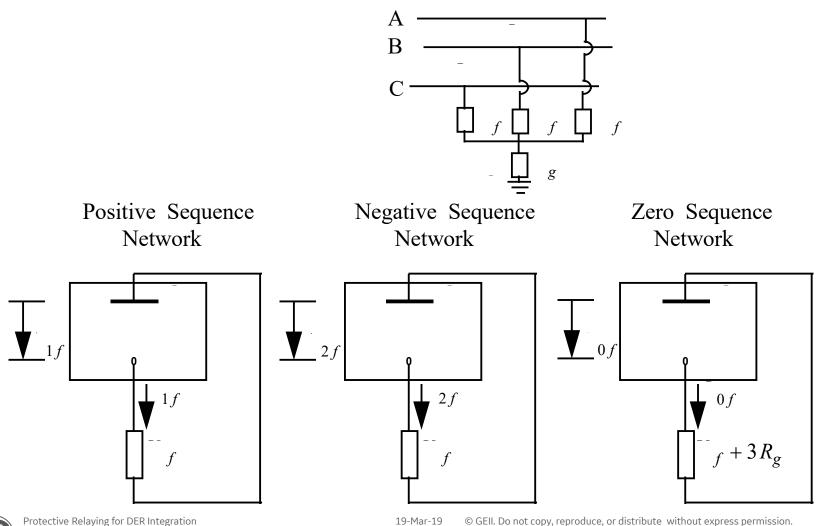
System Faults: Balanced Three Phase





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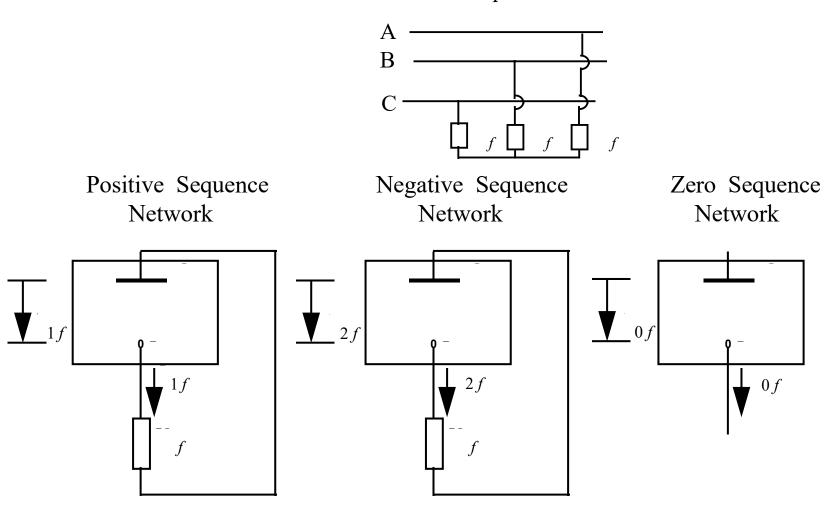
System Faults: Balanced Three Phase involving ground Three-phase-to-ground fault





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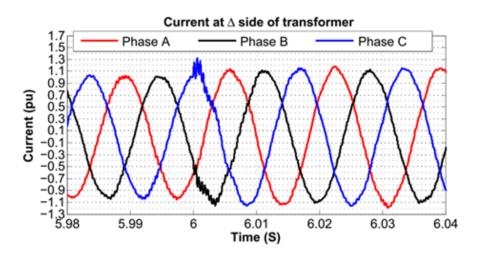
System Faults: Balanced Three Phase without ground Three-phase fault

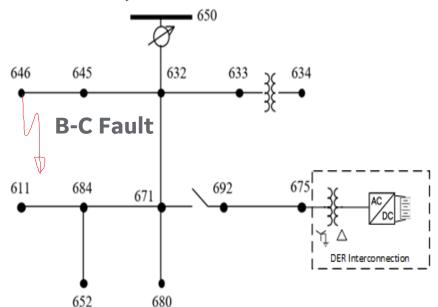


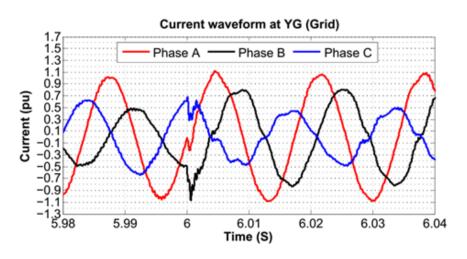


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Protection Issue: Case Study 2





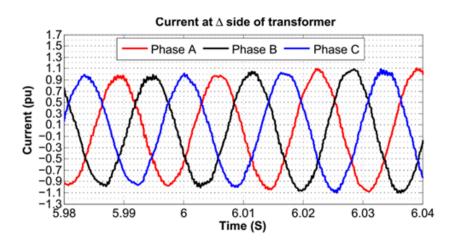


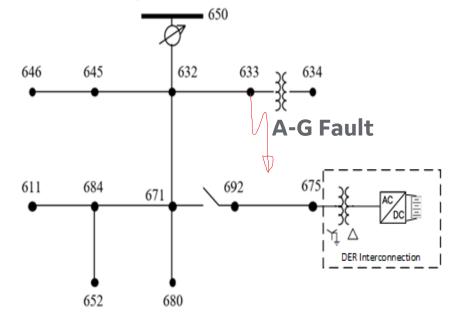
Reference:

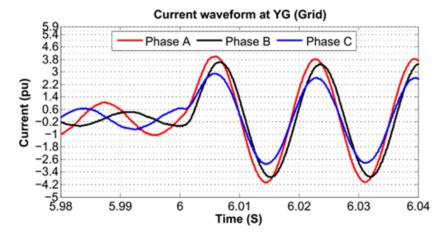
IEEE Power System Relaying and Control (PSRC) Committee WG C30 - working document. Work in progress.



Protection Issue: Case Study 2





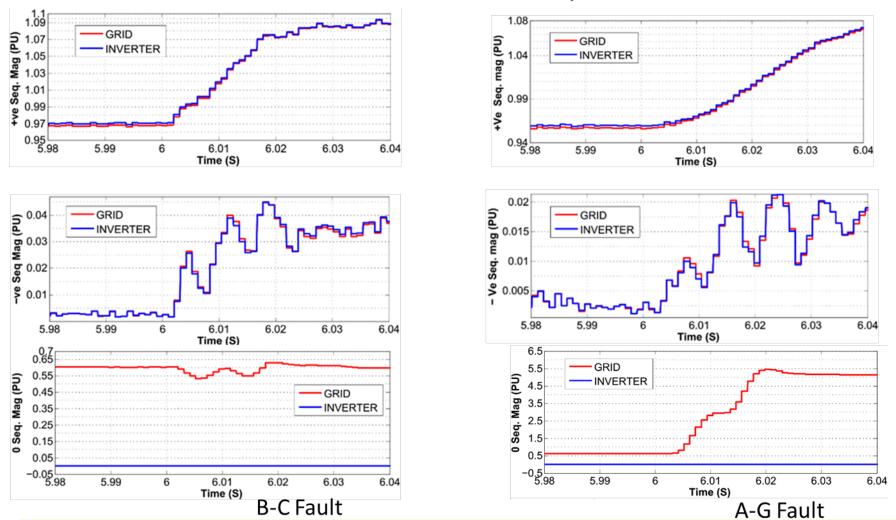


Reference:

IEEE Power System Relaying and Control (PSRC) Committee WG C30 - working document. Work in progress.



Protection Issue: Case Study 2



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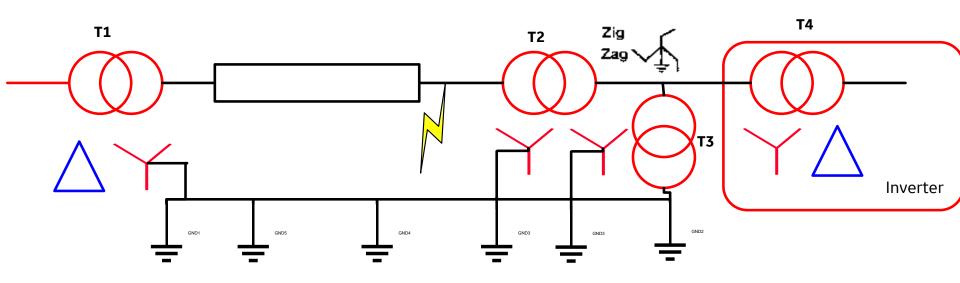
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– permanent faults



Example of DER Interface to the Grid

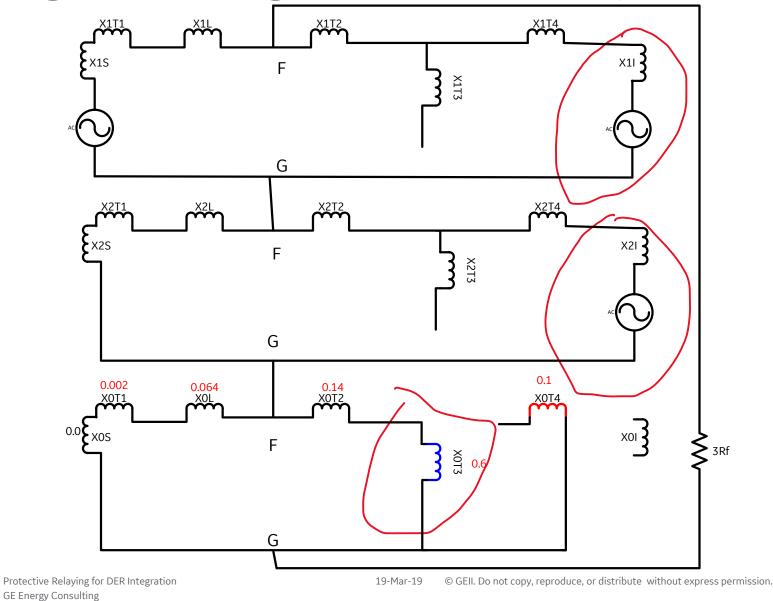


Reference:

Soonwook Hong, Il Do Yoo, Terry Bruno J.M. and Michael Zuercher-Martinson, White Paper, "Effective Grounding for PV Plants", SRCW00101, Solectria Renewables.



Single-line-to-ground-fault Analysis



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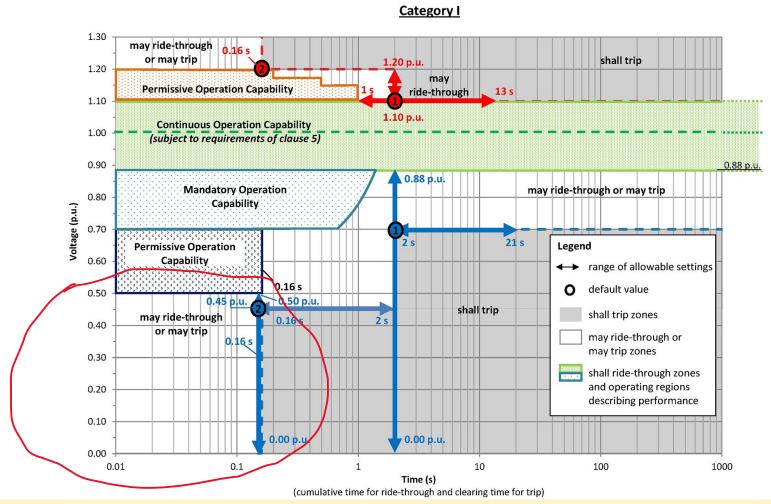
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Voltage Ride Through Capability



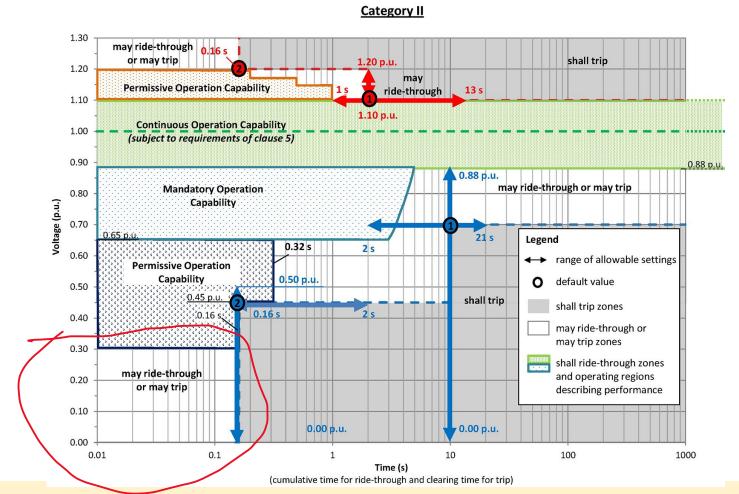
Reference:

IEEE Standard for Interconnection and Interoperability of Distributed Energy Resources with Associated Electric Power Systems Interfaces, IEEE Std. 1547[™]-2018.



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Voltage Ride Through Capability



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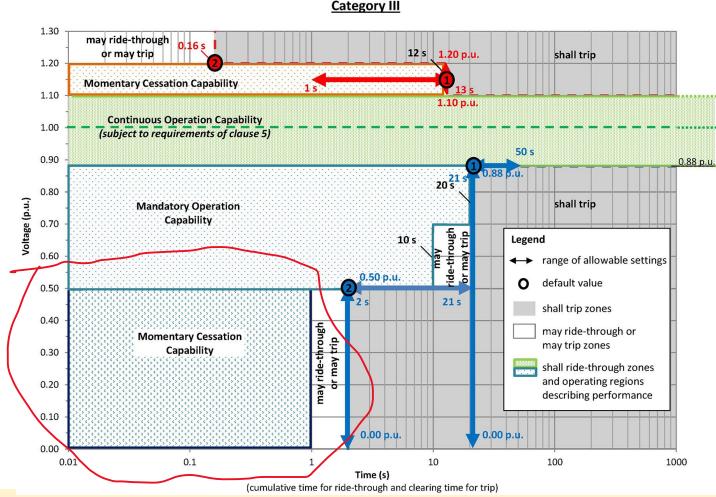
IEEE Standard for Interconnection and Interoperability of Distributed Energy Resources with Associated Electric Power Systems Interfaces, IEEE Std. 1547[™]-2018.



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Voltage Ride Through Capability



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DER Cessation Functionality

- No active power delivery
- Limited reactive power exchange
- Inverter is blocked (Inverter can take 5-15 minutes if tripped and DER have to follow reentry process)
- DER Cessation function is better than tripping
 - Fault usually happens in a small part of the system (line/bus/equipment) and proper clearance of fault disconnects faulted part of the system with some load and/or generation and related DERs
 - Voltage will be dipped in healthy part of the system during fault
 - Tripping of DERs in healthy part of the system due to voltage dip will disturb load-generation balance after fault clearance and can cause grid disturbance



What We Discussed

- Why we need protection
- How we provide equipment protection
 Models and Examples
- How we provide System Protection
 Example



Imagination at work

